86070-4SOCT60

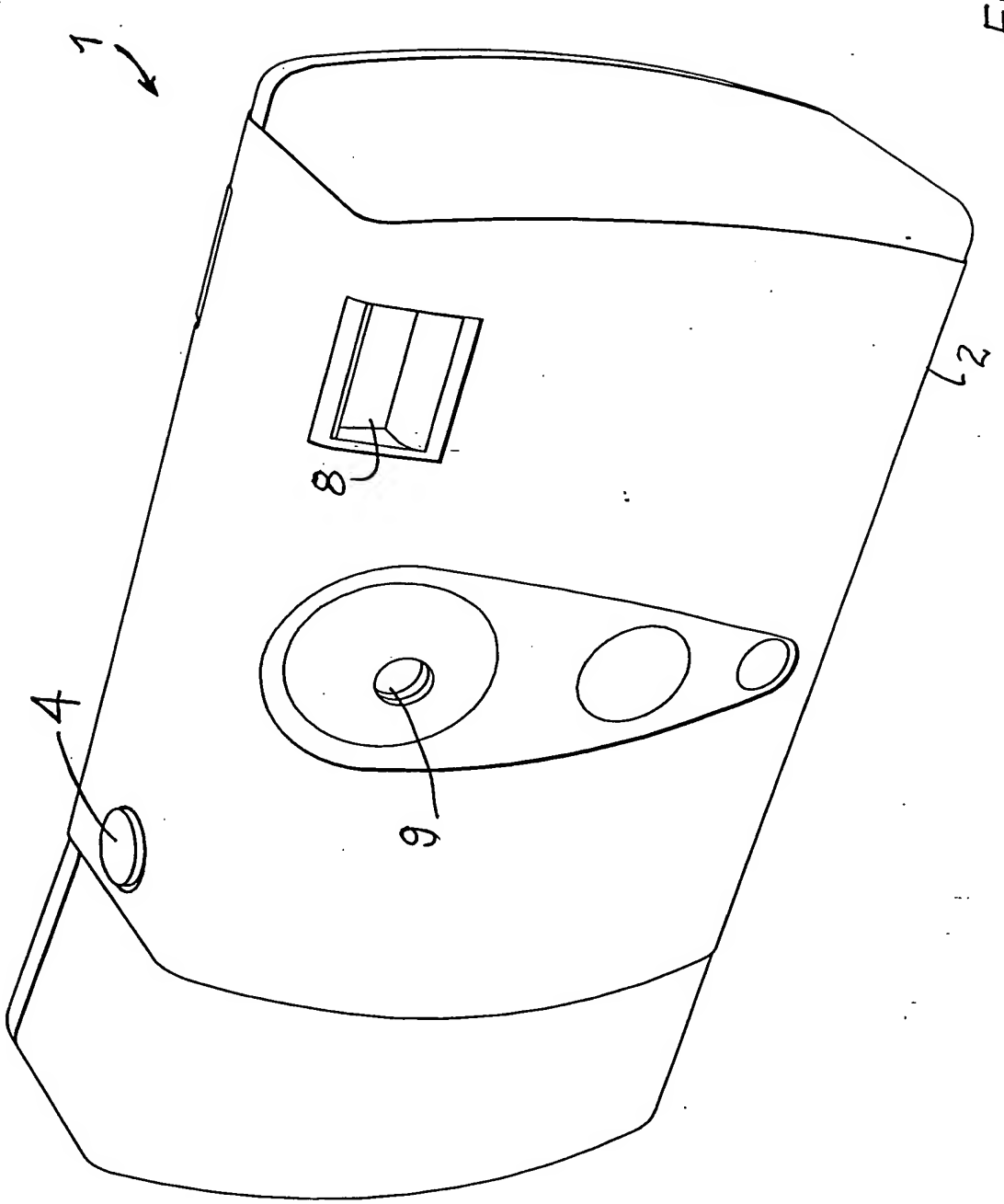

Fig. 1

86870-4602T60

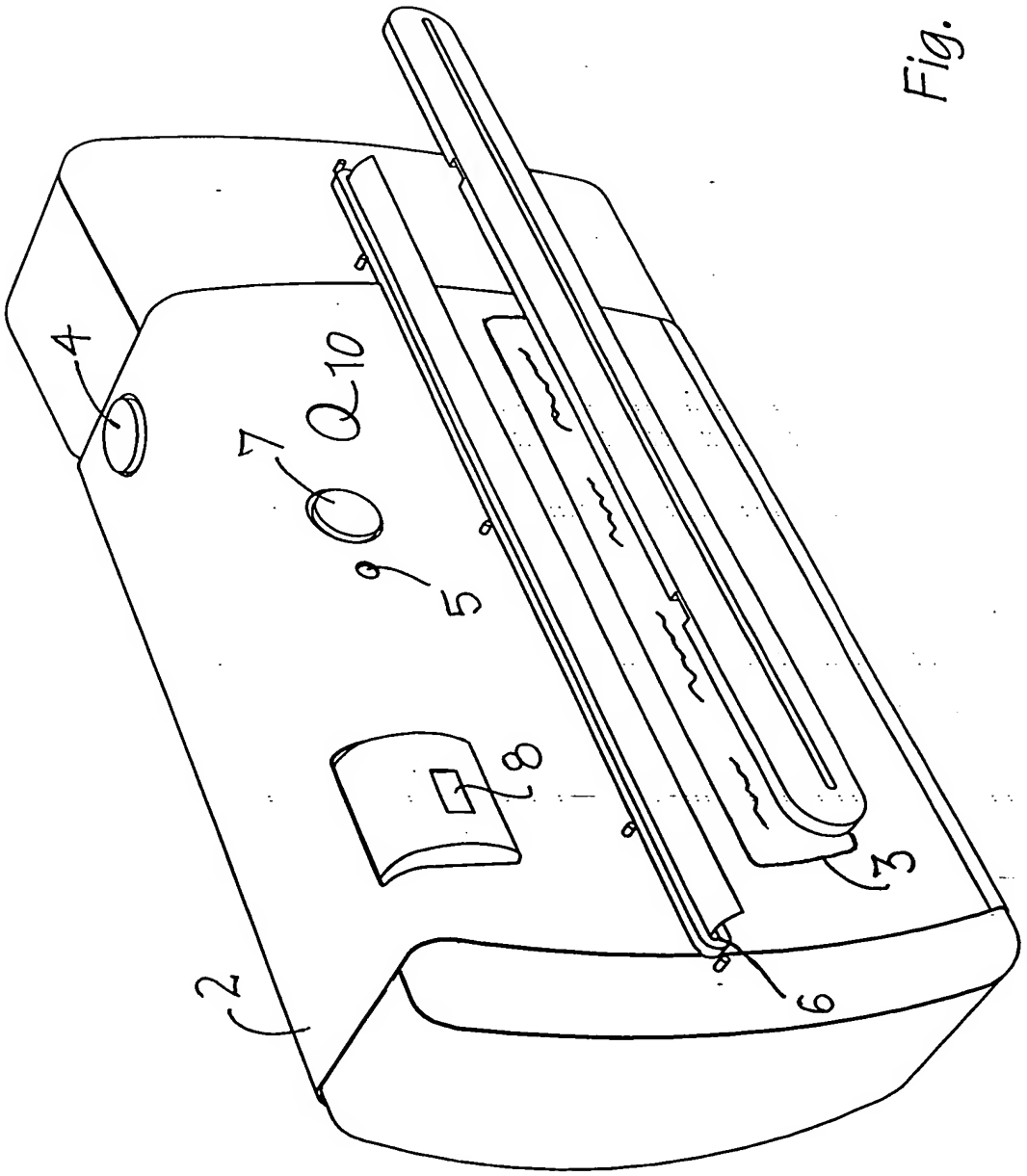

Fig. 2

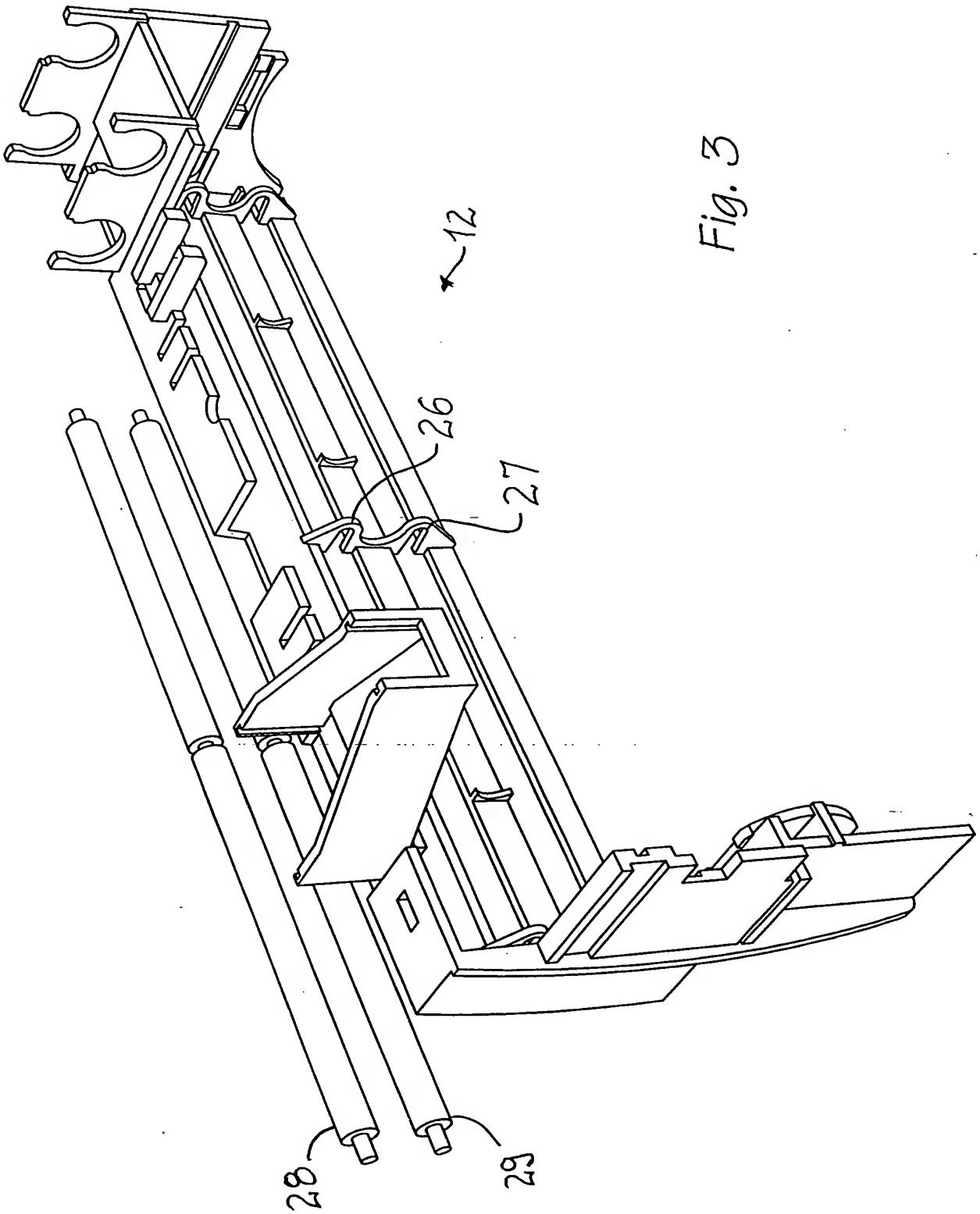

Fig. 3

EPO - Munich
20
07. 2006

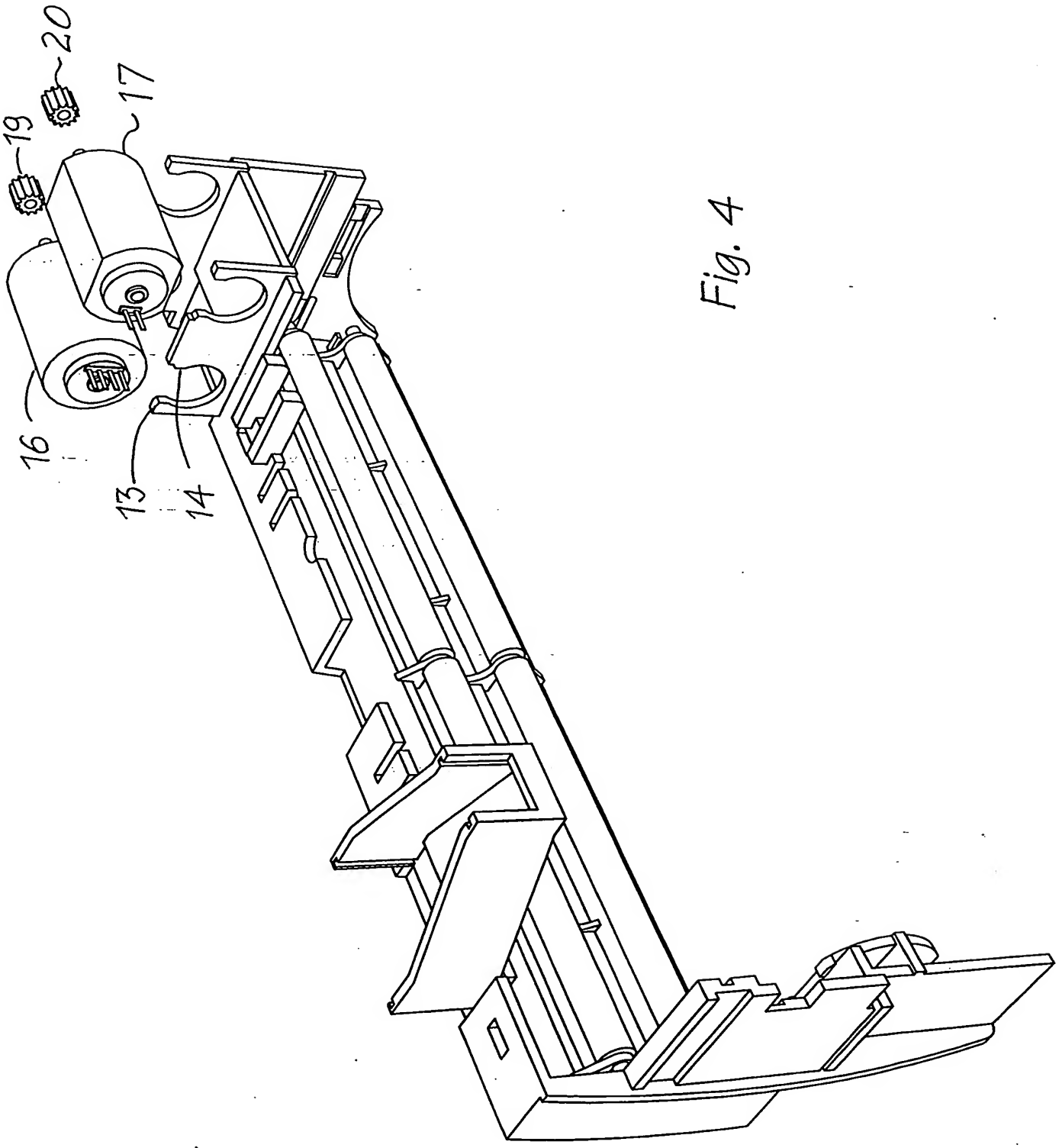

Fig. 4

EP 0 760 694 B1

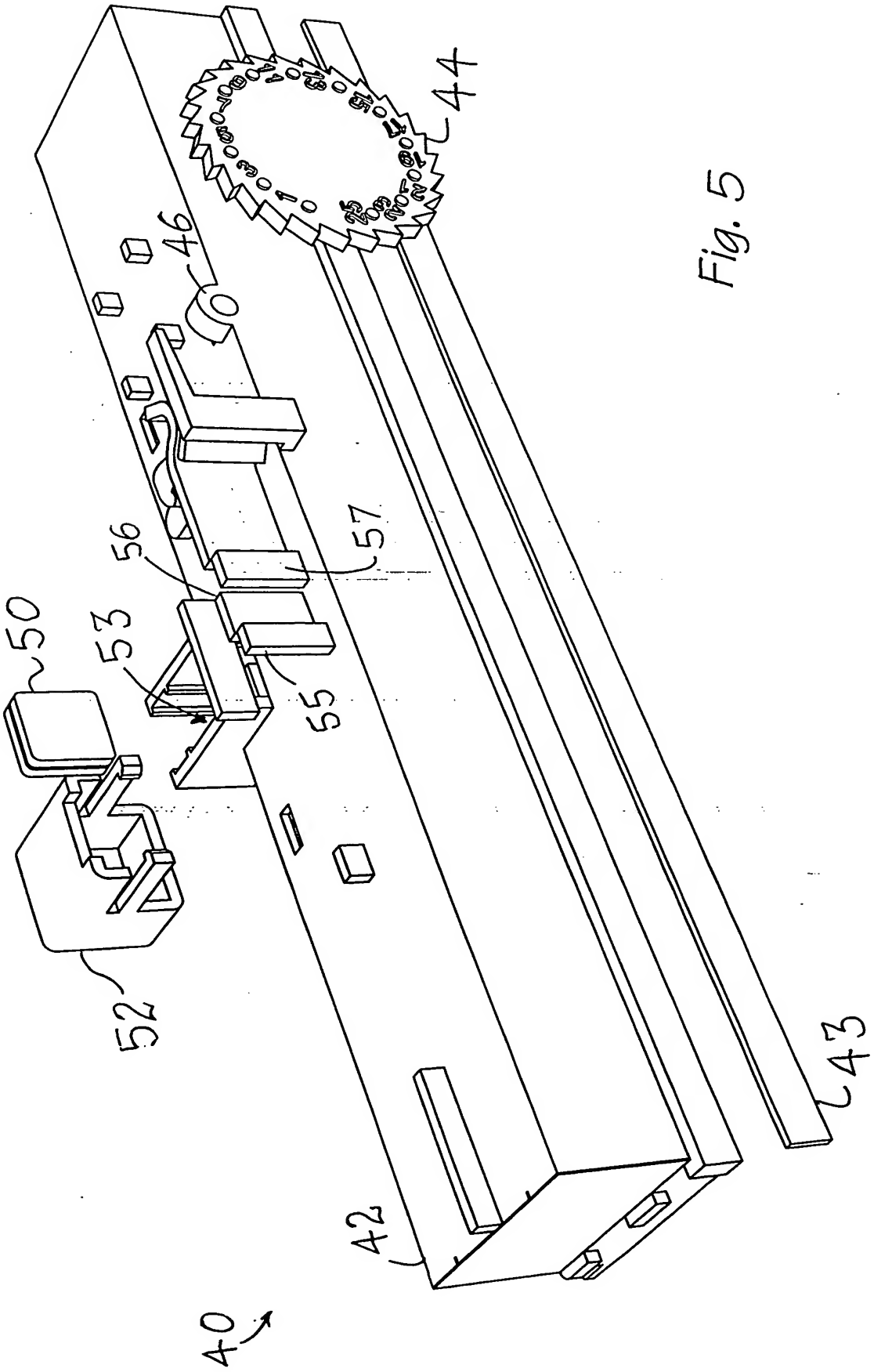

Fig. 5

860720-060660

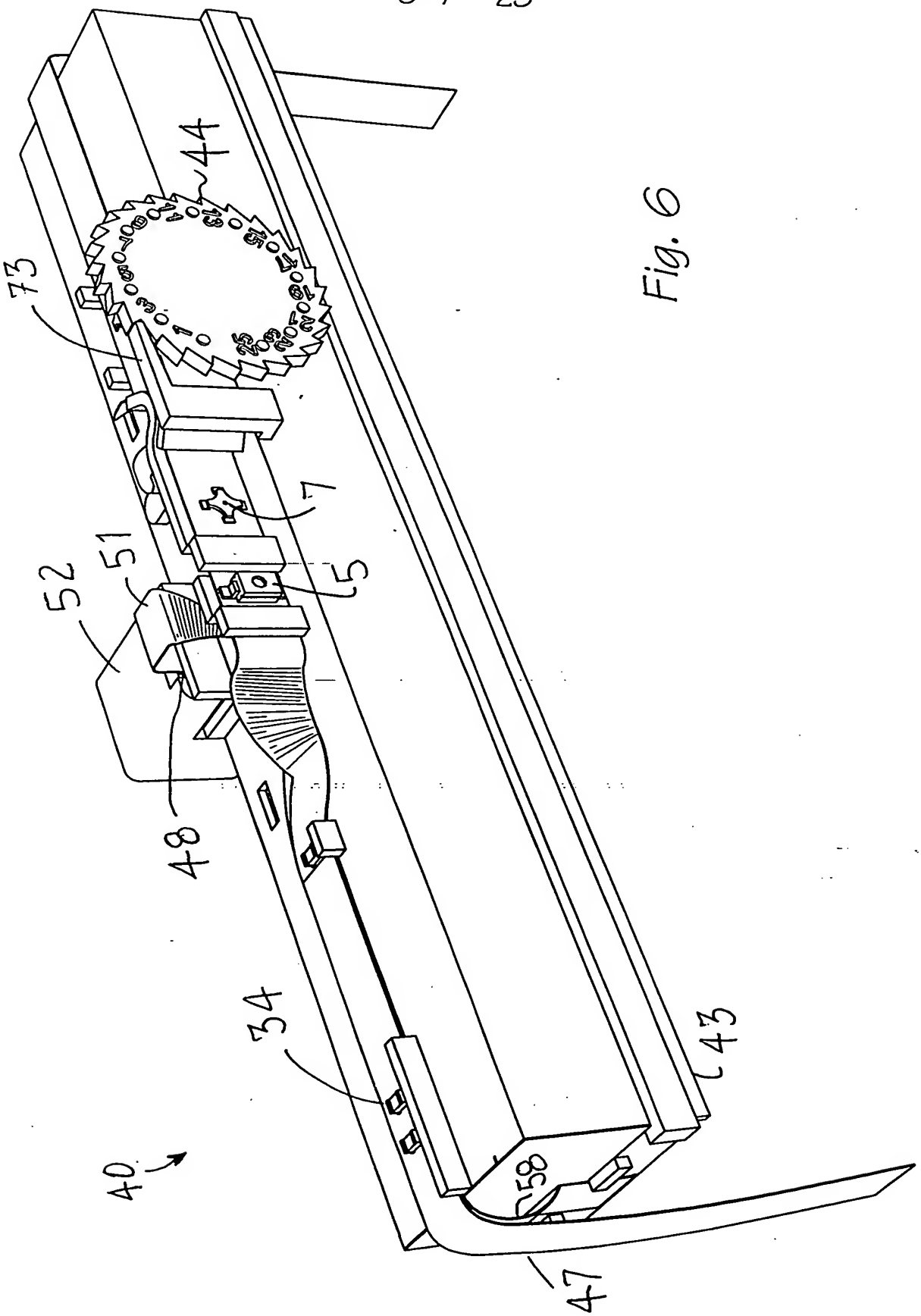

Fig. 6

WO 2004/067870 PCT/US2004/002168

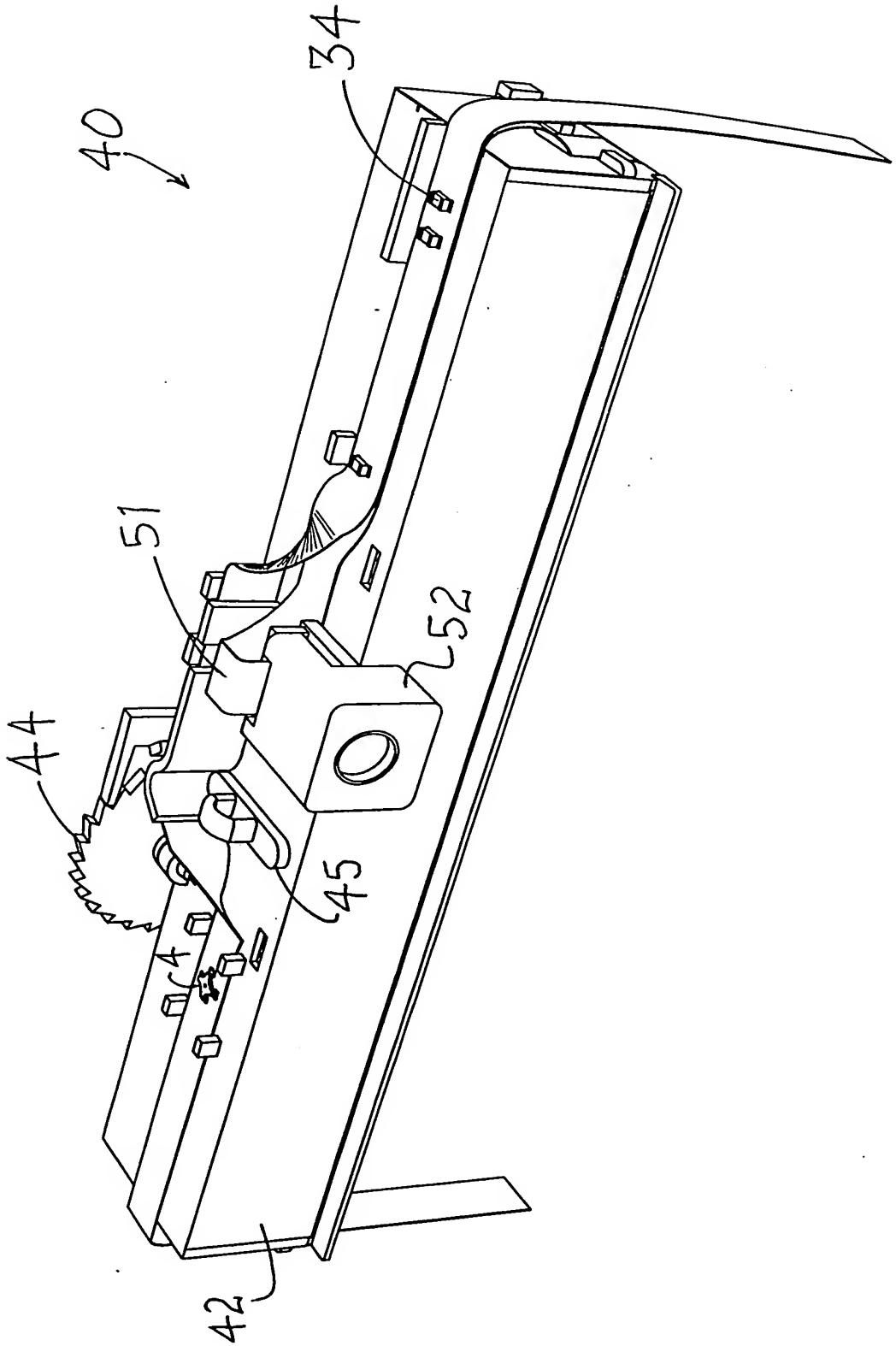

Fig. 7

86070.4698US1

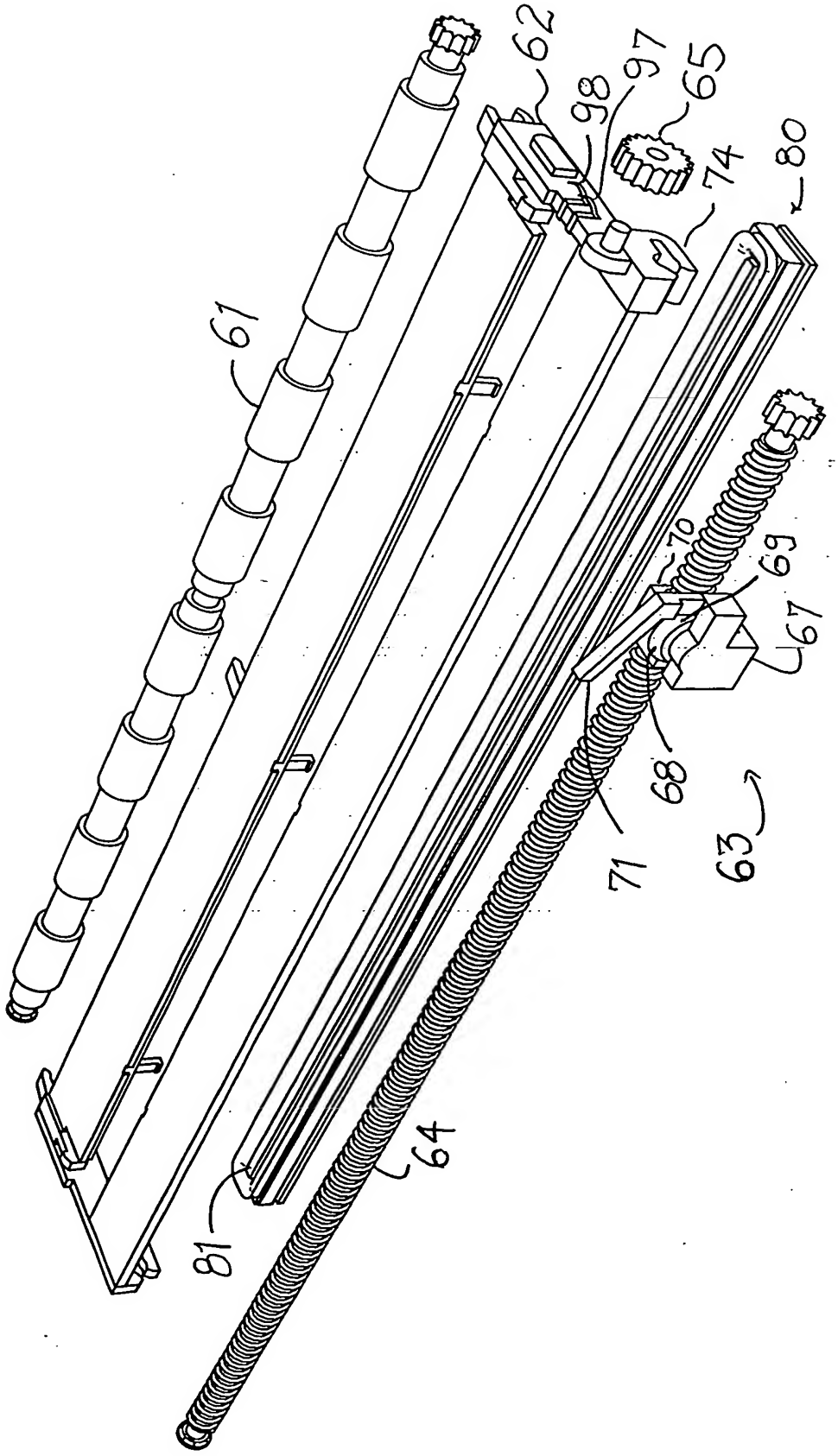

Fig. 8

SUBSTITUTE SHEET (RULE 26)

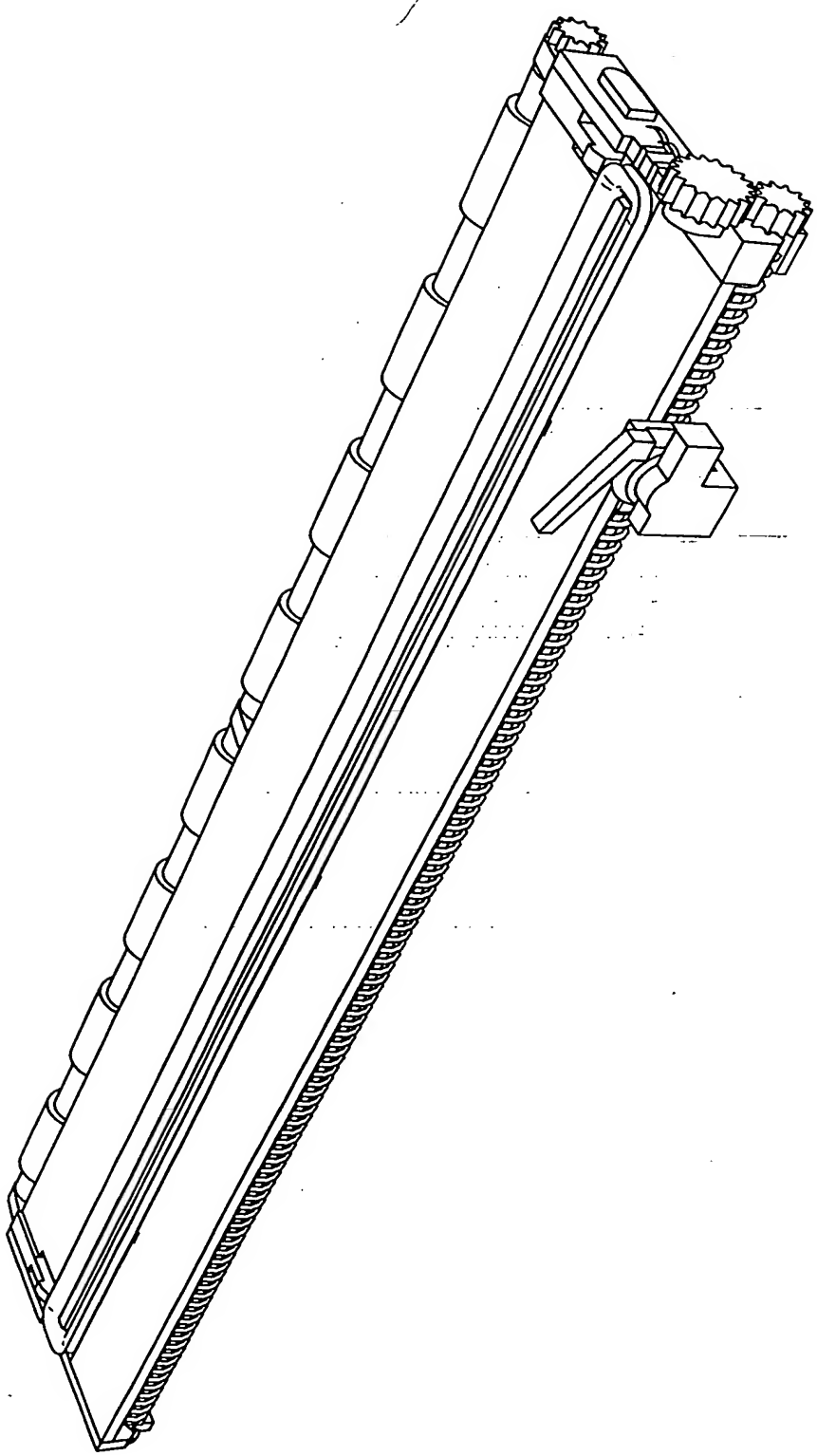

Fig. 9

0941394-A2 20090730

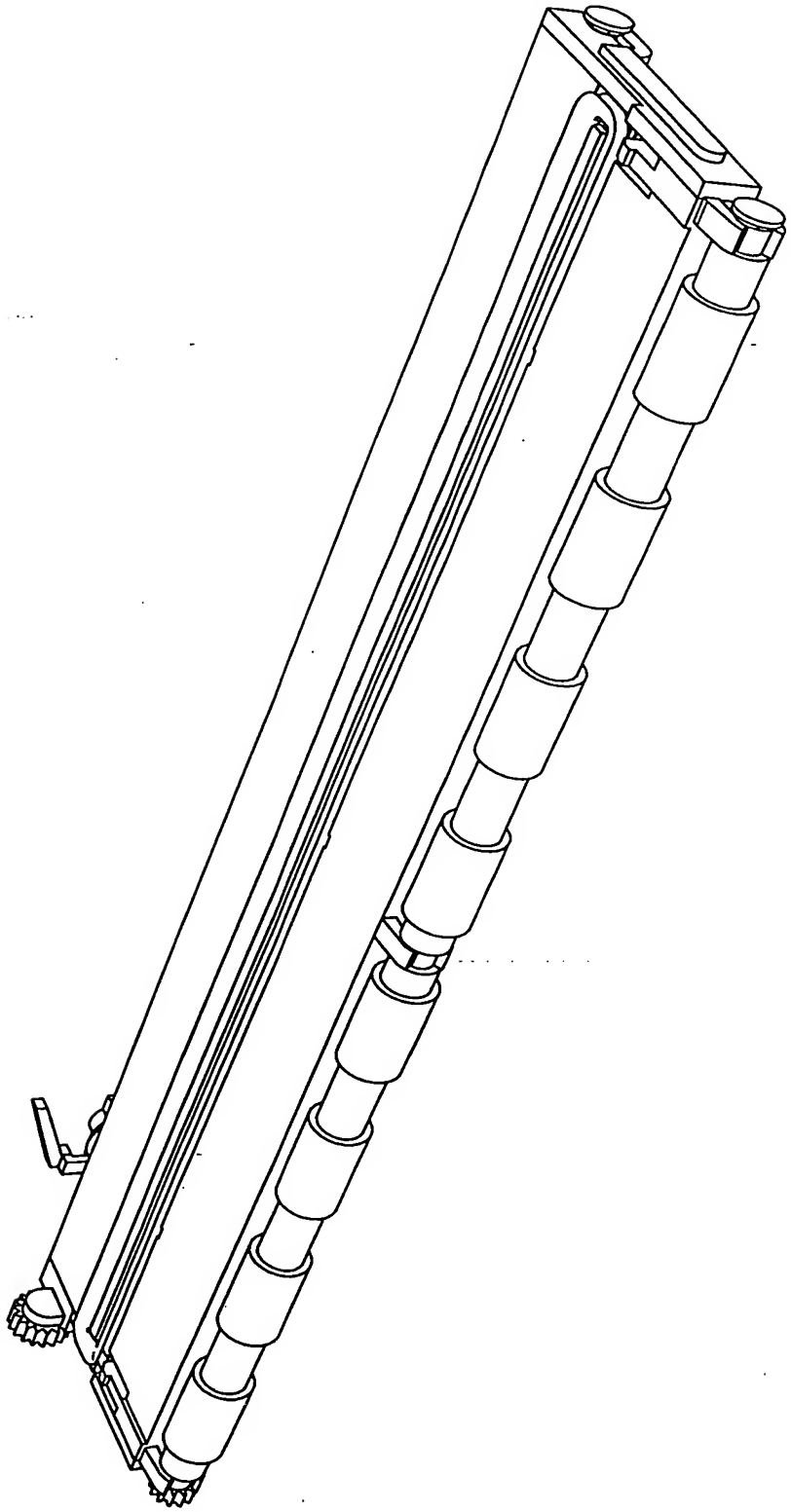

Fig. 10

EP 0 704 580 B1

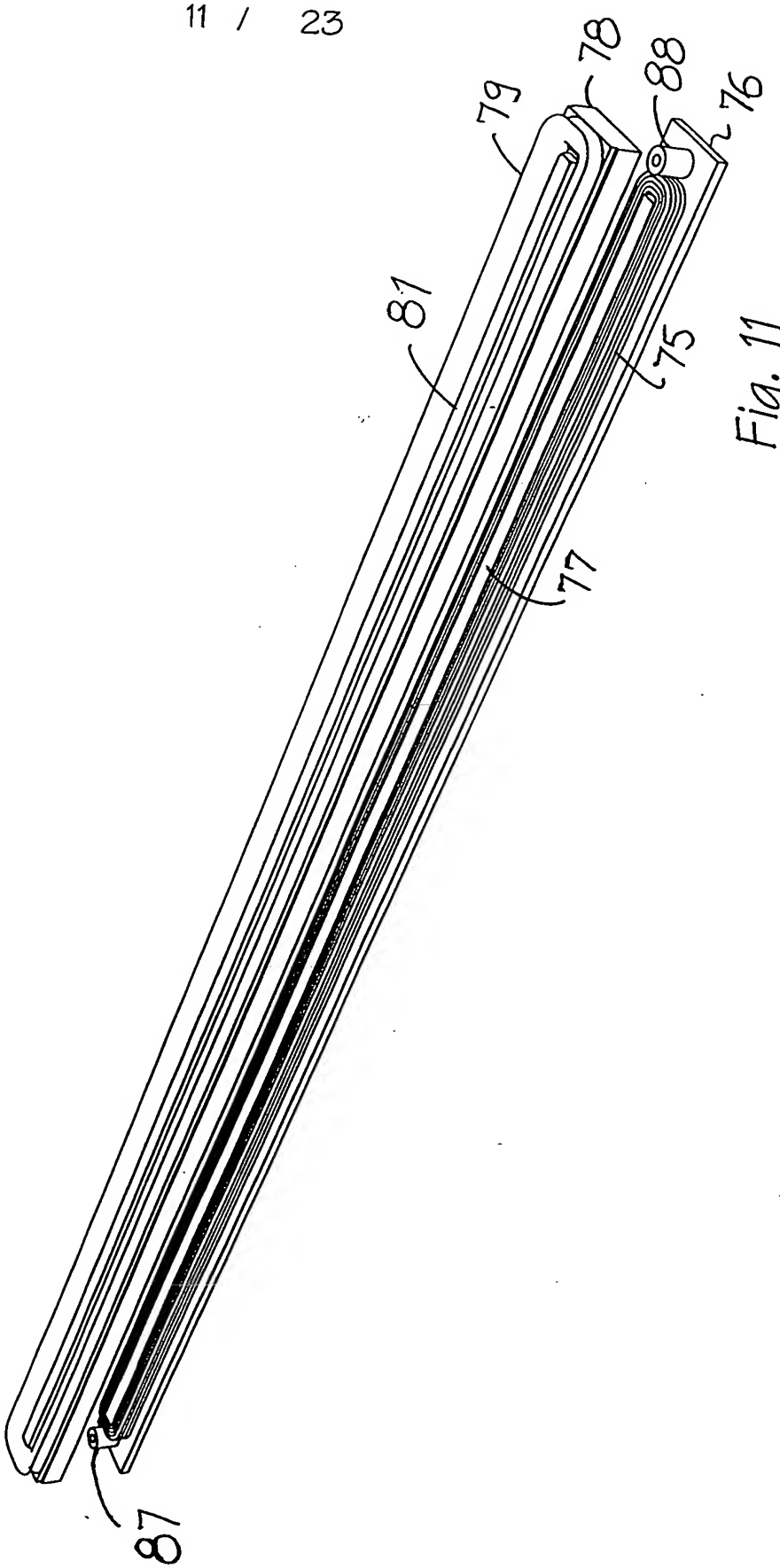

Fig. 11

60624-4502-US

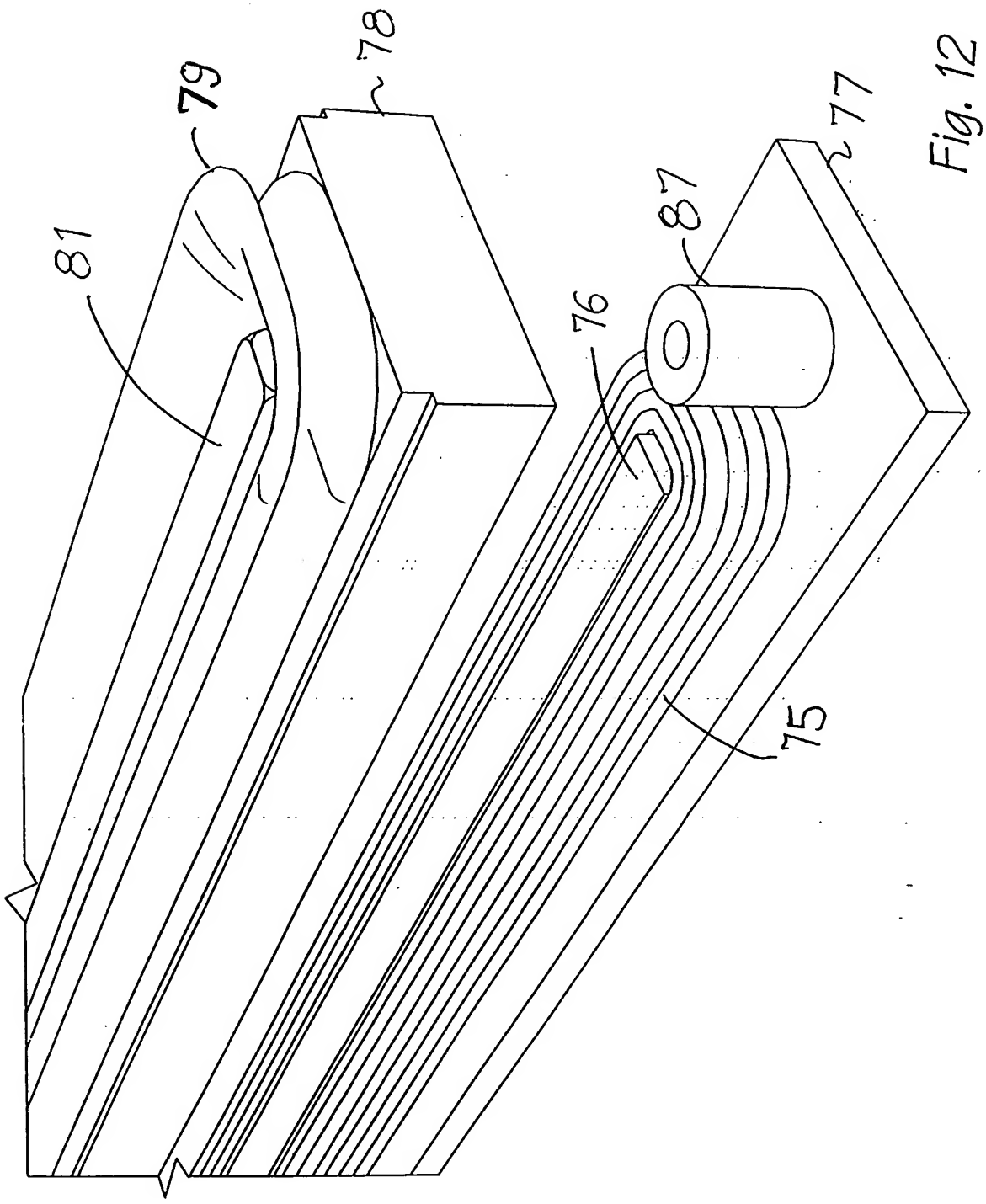

Fig. 12

86074.0*60CT60

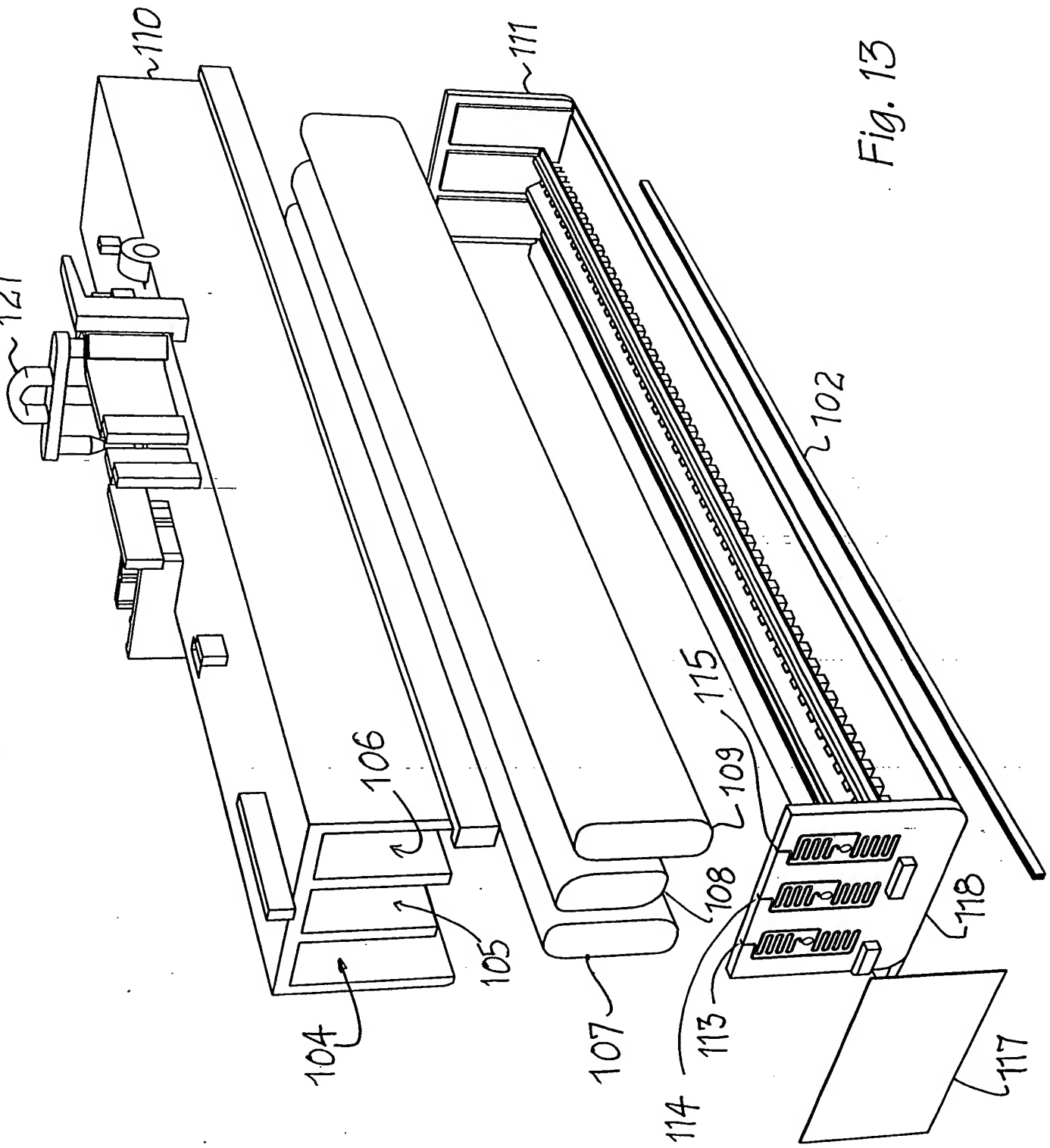

Fig. 13

SUBSTITUTE SHEET (RULE 26)

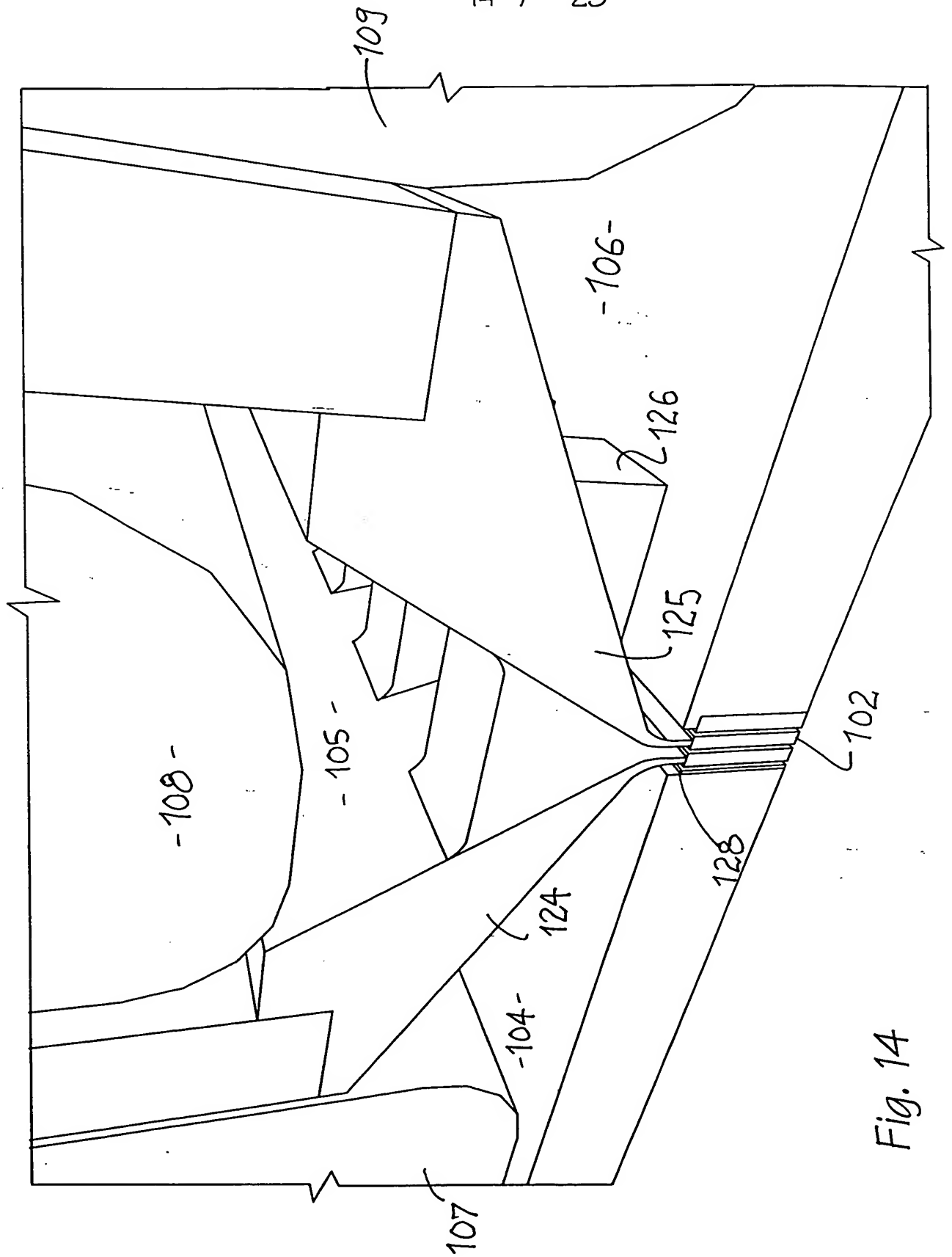

Fig. 14

86F70+60ET50

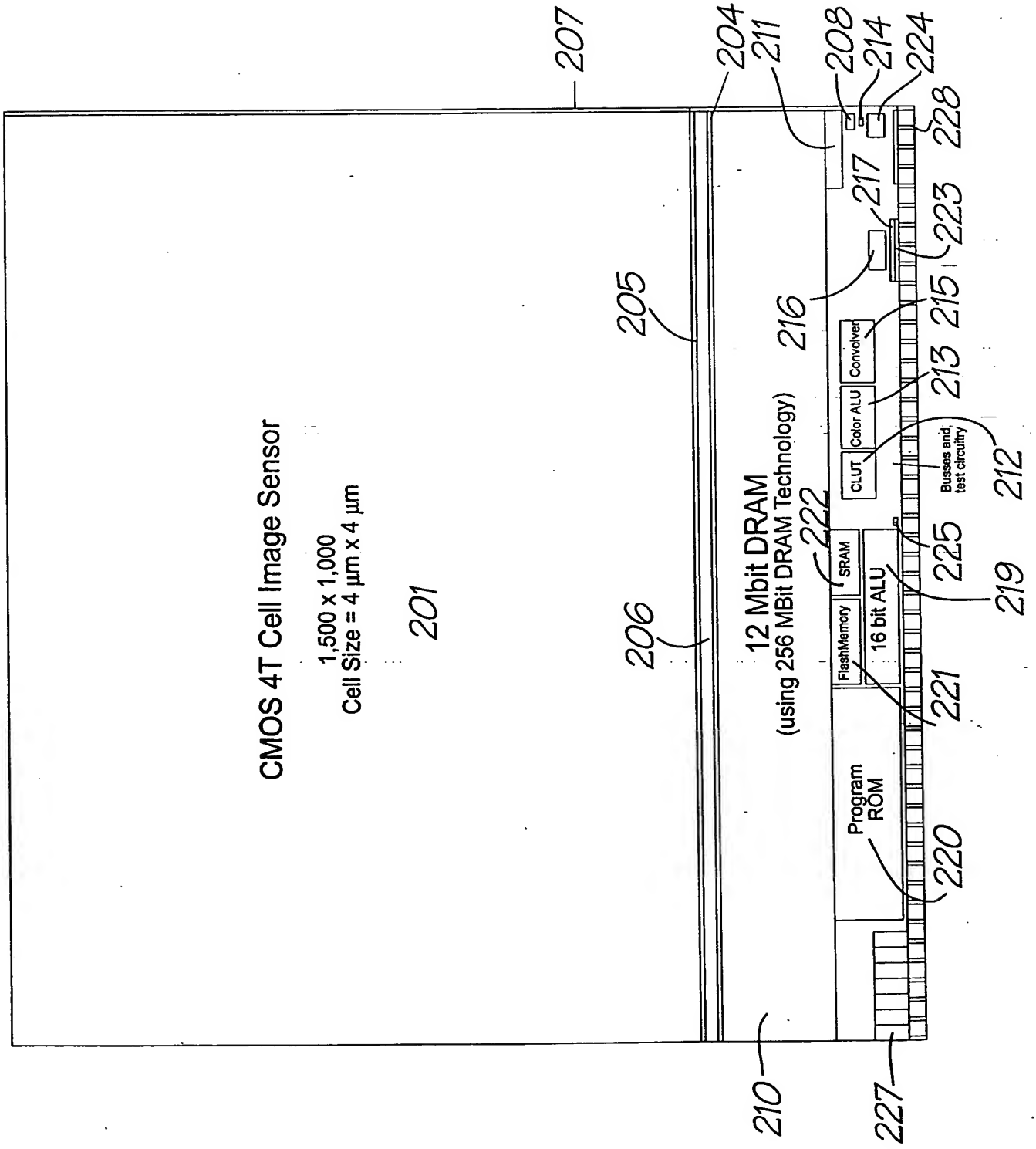
CMOS 4T Cell Image Sensor
1,500 x 1,000
Cell Size = 4 µm x 4 µm
201
12 Mbit DRAM
(using 256 MBit DRAM Technology)
Convolver
Color ALU
CLUT
Busses and test circuitry
SRAM
FlashMemory
16 bit ALU
Program ROM
207
204
211
208
214
224
228
217
223
216
215
213
205
212
222
225
219
206
221
220
210
227

Fig. 15

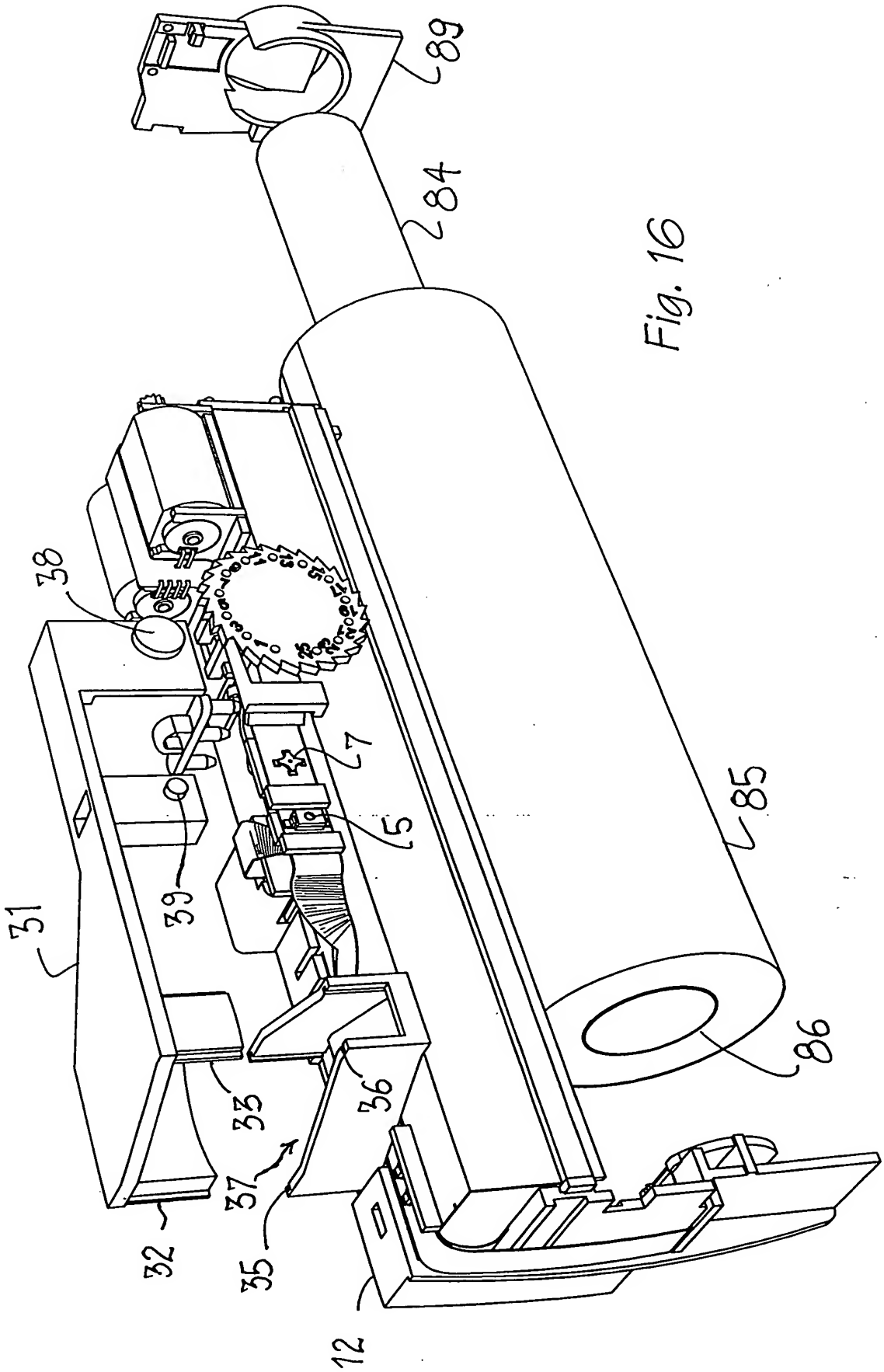

Fig. 16

WO 97/04950 PCT/SE96/00969

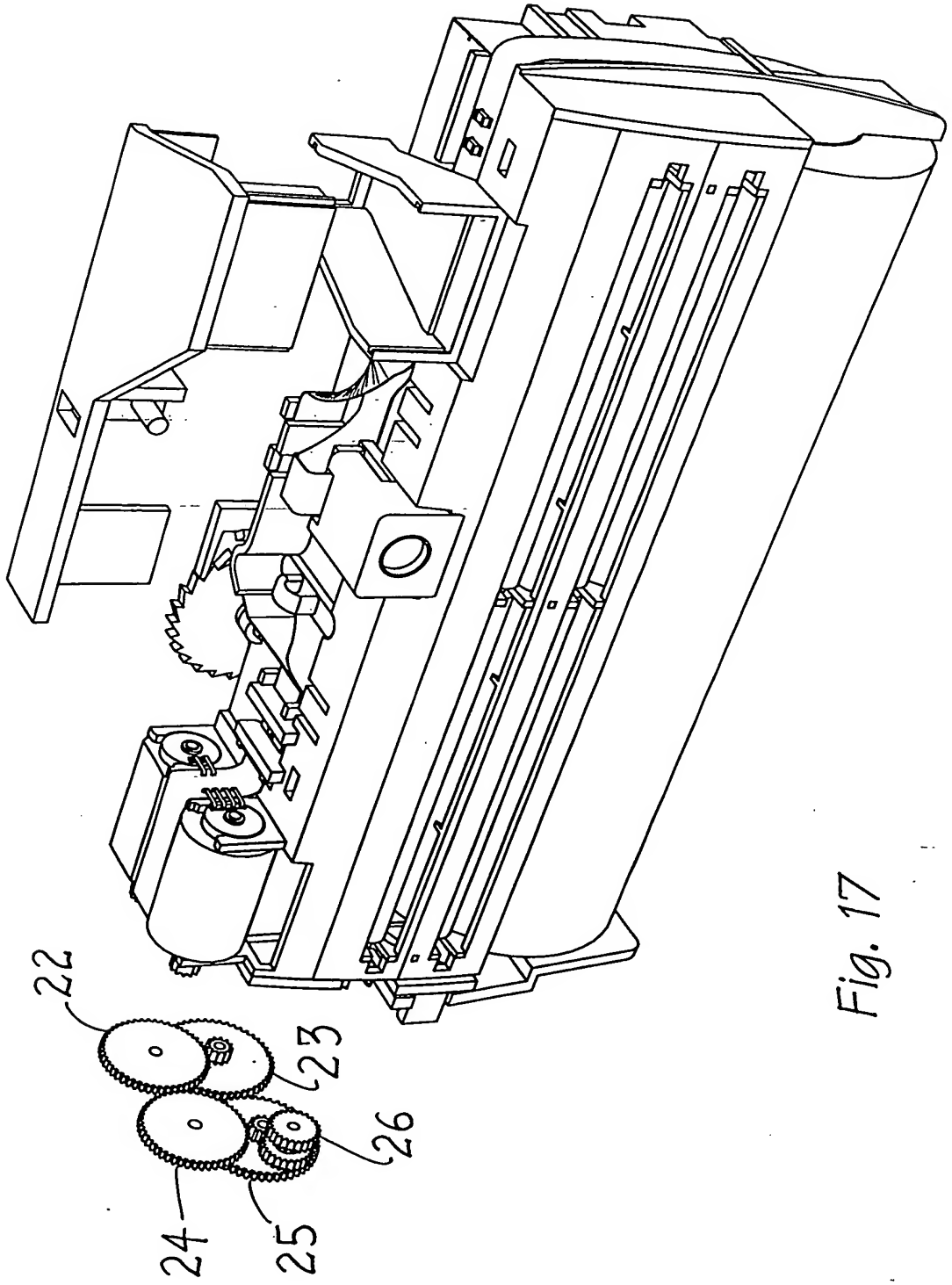

Fig. 17

86070 "46067560

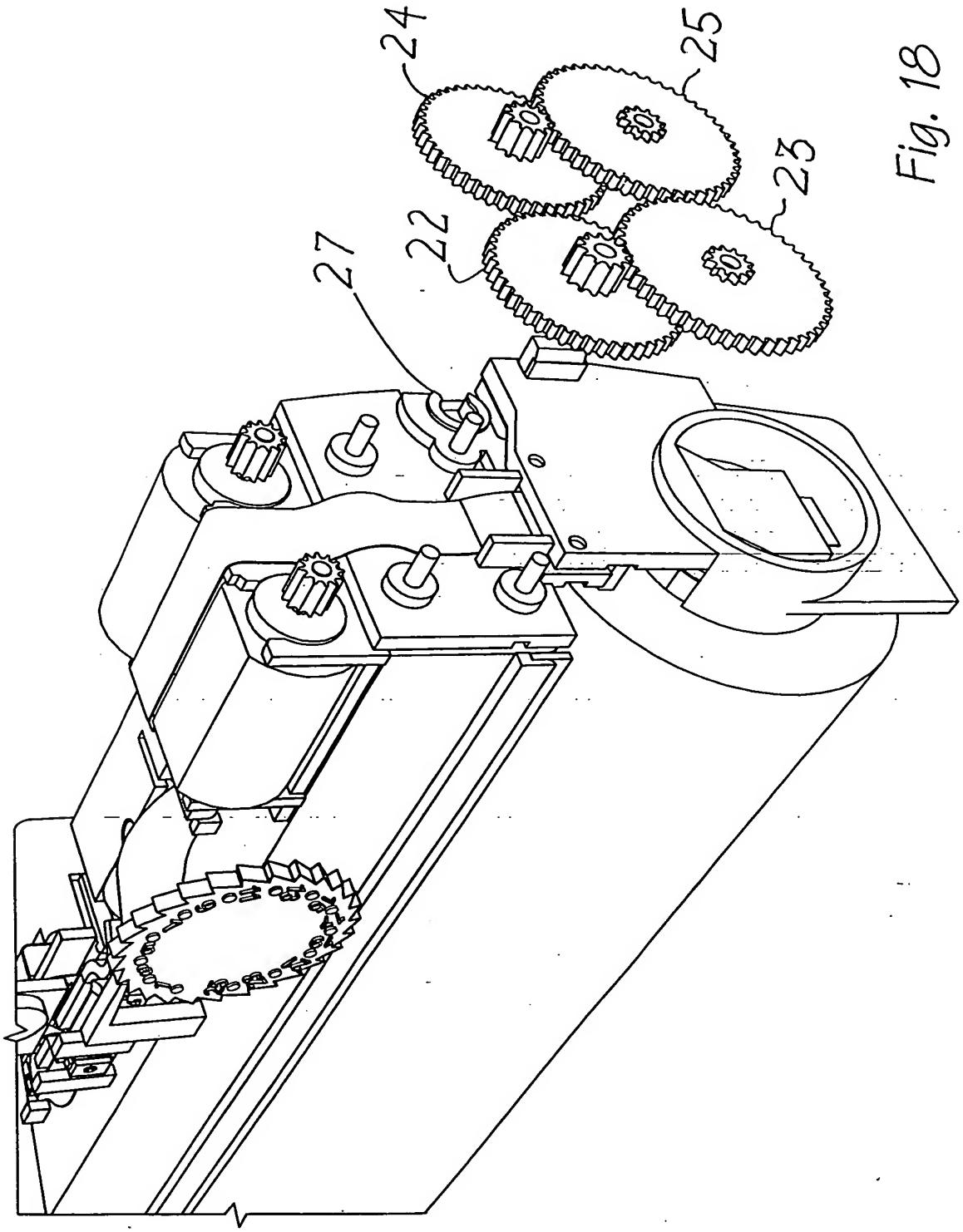

Fig. 18

86017/0 4609T50

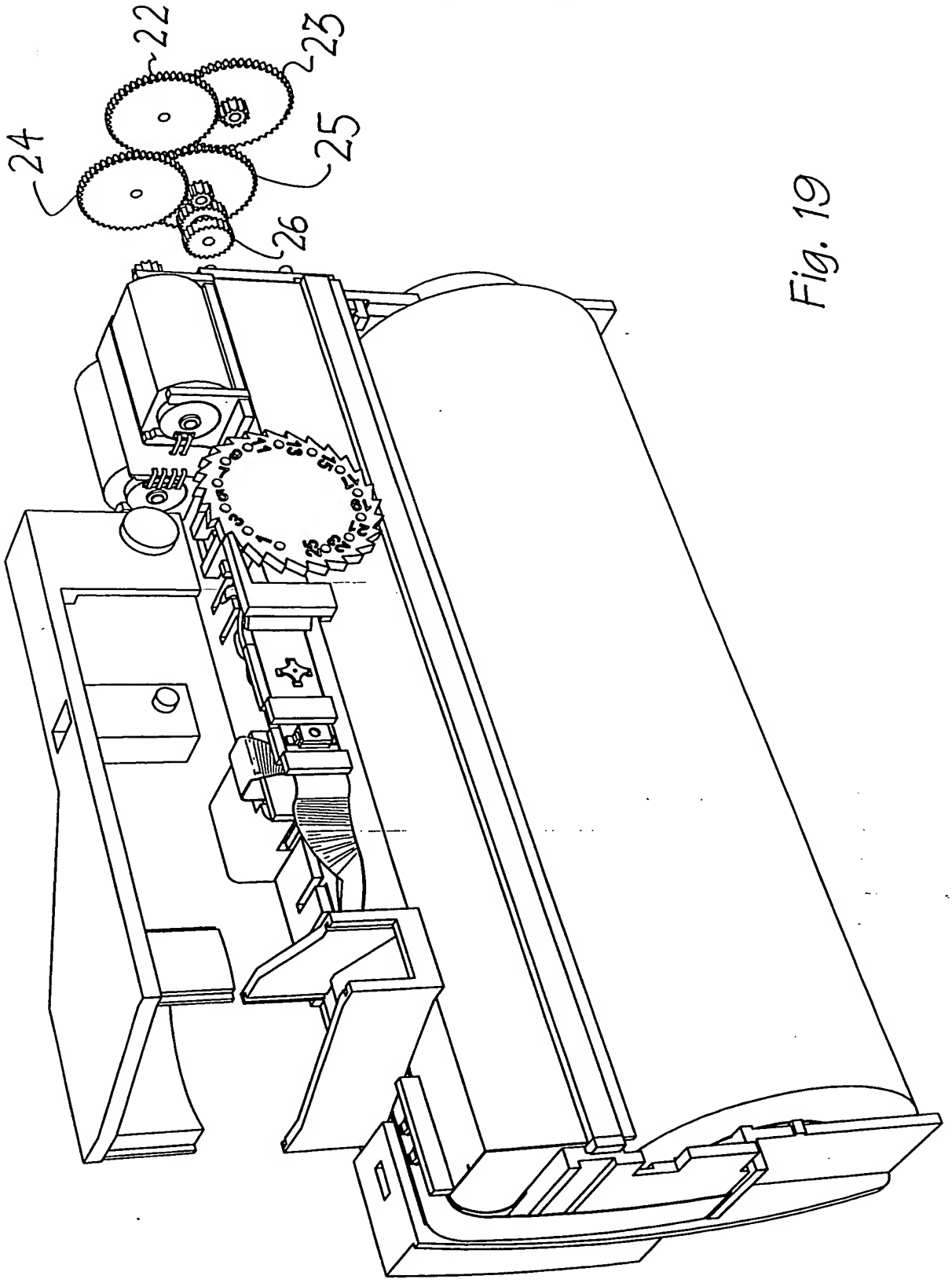

Fig. 19

86076/0 "Hoechst"

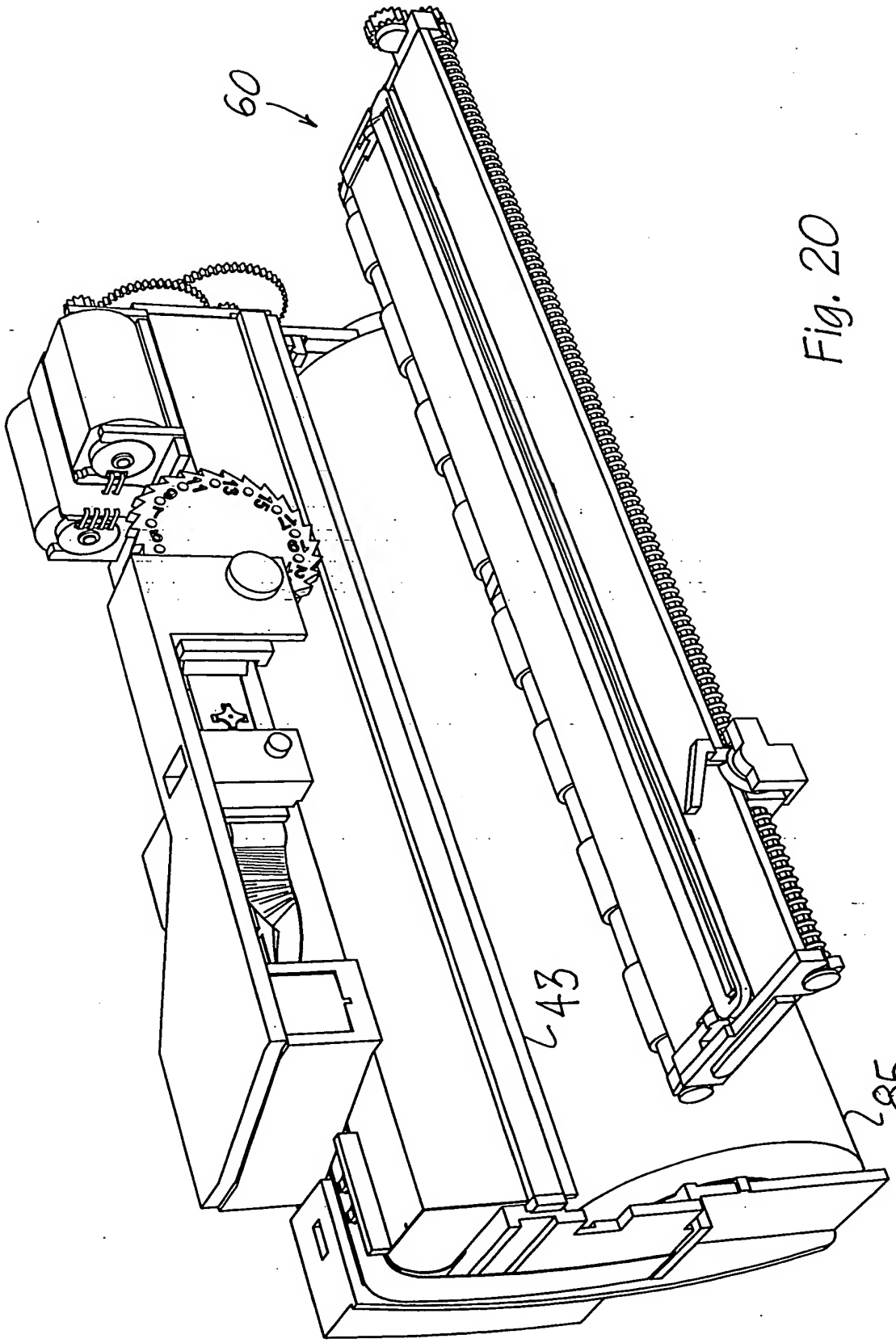

Fig. 20

EP 0 771 658 A1

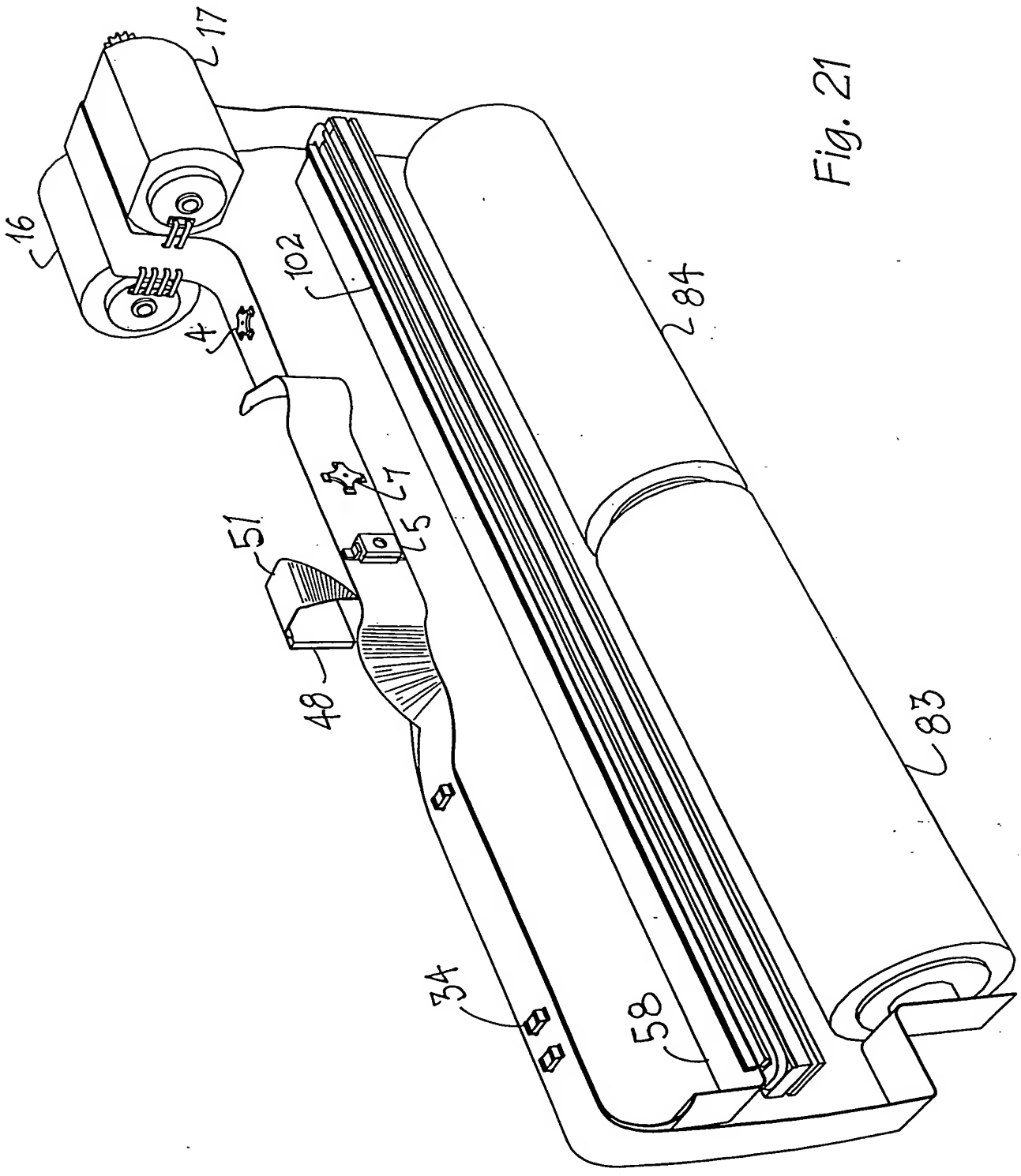

Fig. 21

86070"46ae760

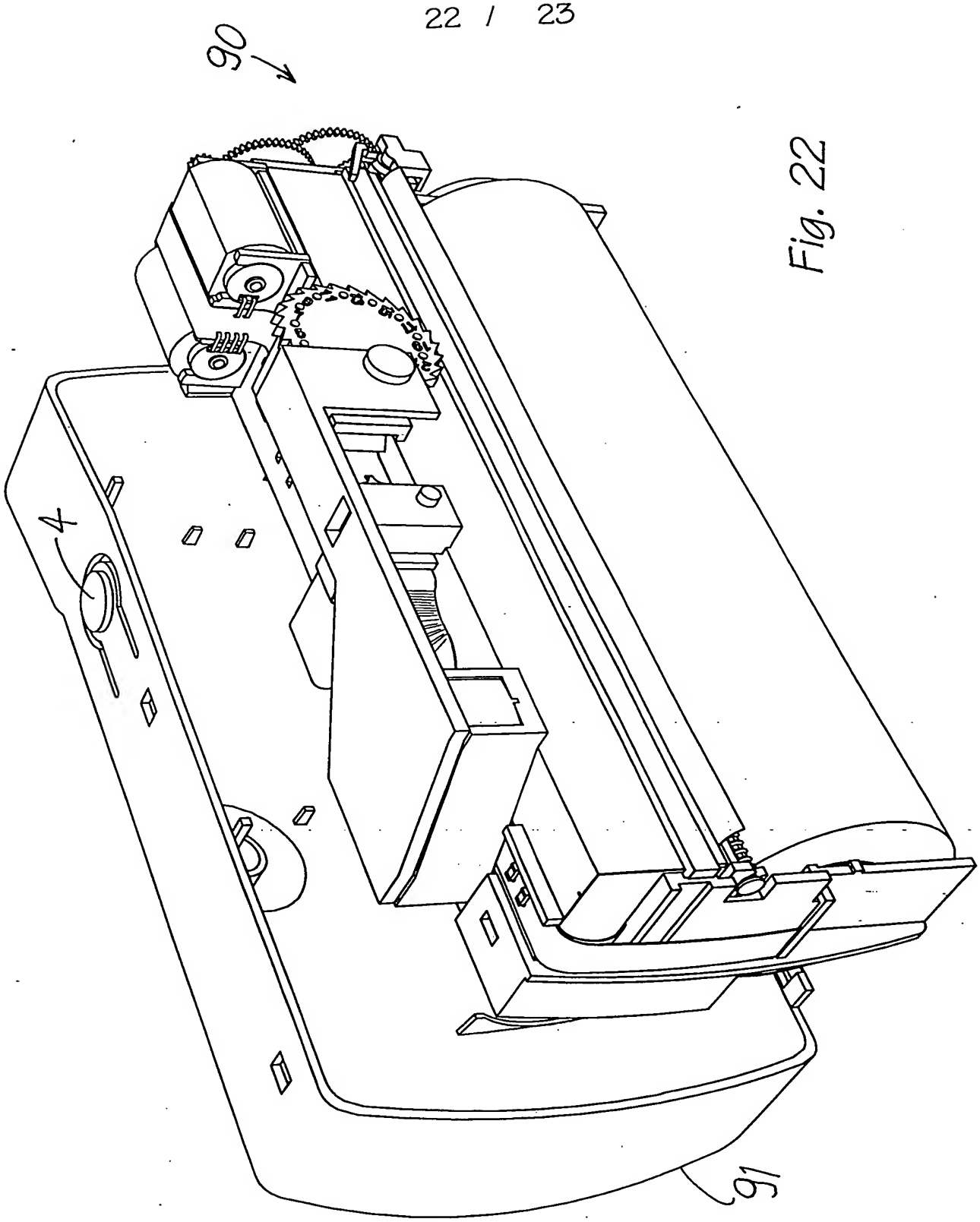

Fig. 22

EP 0 709 464 A2

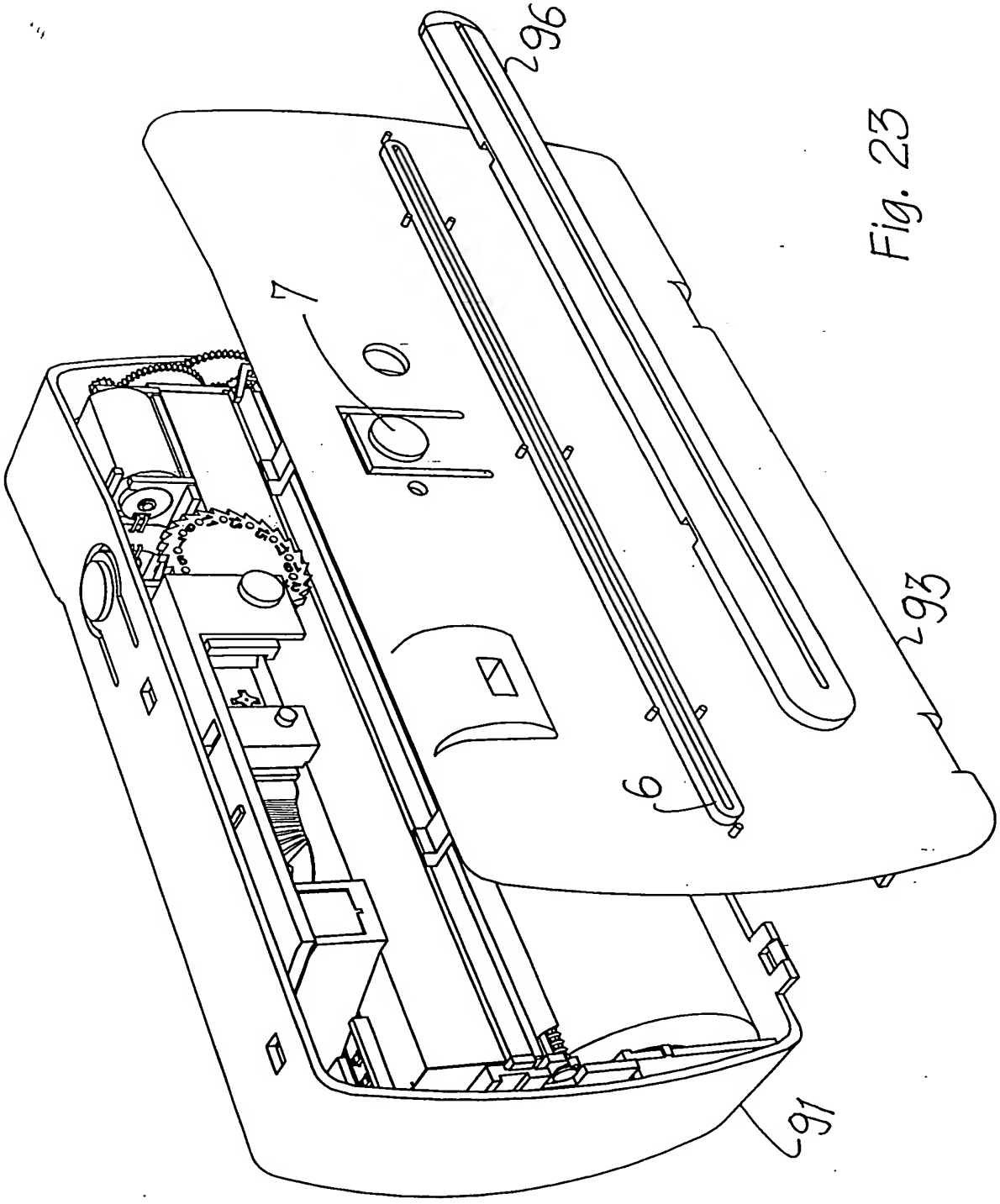

Fig. 23

86072070 "46061760